Table of Contents

Chapter 1: Getting Started

Introduction **1-2**
The Purpose of this Manual 1-2
Who Should Read this Manual 1-2
Where to Begin 1-2
Supplemental Manuals 1-2
How this Manual is Organized 1-3
Technical Assistance 1-3

DL305 System Components **1-4**
DirectSOFT Programming for Windows™ 1-4
Handheld Programmer 1-4
DL305 System Diagrams 1-4

***Direct*LOGIC™ Part Numbering System** **1-8**

Steps to a Successful System **1-10**
Step 1: Review the Installation Guidelines 1-10
Step 2: Understand the CPU Setup Procedures 1-10
Step 3: Understand the I/O System Configurations 1-10
Step 4: Review the I/O Selection Criteria 1-10
Step 5: Determine the I/O Module Specifications and Wiring Characteristics 1-10
Step 6: Understand the System Operation 1-11
Step 7: Review the Programming Concepts 1-11
Step 8: Choose the Instructions 1-11
Step 9: Understand the Maintenance and Troubleshooting Procedures 1-11

Chapter 2: Installation and Safety Guidelines

Safety Guidelines **2-2**
Plan for Safety 2-2
Safety Techniques 2-2
Orderly System Shutdown 2-3
System Power Disconnect 2-3

Panel Design Specifications **2-4**
Environmental Specifications 2-6
Power 2-6
Agency Approvals 2-7
Enclosures 2-7

Component Dimensions **2-7**
Component Dimensions Part 2 2-9

Base Mounting Dimensions **2-10**

Installing Components in the Base **2-10**
Base Wiring 2-11
Expansion Base Wiring 2-11

I/O Wiring **2-12**
I/O Wiring Guidelines 2-12
Wiring the Different Module Types 2-13

Chapter 3: DL330/DL330P/DL340 CPU Specifications

Overview **3-2**
DL330 CPU Features 3-2
DL330P CPU Features 3-2
DL340 CPU Features 3-2
CPU Hardware Features **3-2**
CPU Specifications **3-3**
Selecting CPU Memory Options **3-4**
Internal Retentive Memory 3-4
External Program Storage 3-4
Volatile and Non-volatile Memory 3-4
Program Storage Memory Types (Internal) 3-5
Storing Programs on UVPROMs 3-6
Setting up the PROM Writer Unit 3-7
Copying a Program From the CPU RAM to a UVPROM 3-7
Copying a Program From the UVPROM to the CPU RAM 3-8
Comparing a Program From the UVPROM to the CPU RAM 3-8
Erasing a UVPROM 3-8
DL330/DL330P CPU Setup **3-9**
Installing the UVPROM Option in the DL330 / DL330P CPU 3-9
Selecting Retentive Memory for the DL330 / DL330P 3-9
DL330/DL330P Networking 3-9
DL340 CPU Setup **3-10**
Installing the optional UVPROM or EEPROM in the DL340 CPU 3-10
Selecting Retentive Memory for the DL340 3-11
DL340 Port Setup **3-12**
DL340 Baud Rate Selection 3-12
DL340 Network Address Selection 3-12
DL340 RS232C Port (1 and 2) Pin Outs 3-13
DL340 Station Type Selection and Address Ranges 3-13
DL340 Selecting the Response Delay Time 3-13
DL340 Selecting Data Format (ASCII/HEX) 3-13
DL340 Selecting Parity for Port 2 3-13
Battery Backup **3-14**
Memory Battery Backup 3-14
DL330, DL330P, DL340 CPU Battery Replacement 3-14
Installing the CPU **3-15**
CPU Setup and System Functions **3-16**
A Few Things to Know 3-16
What are Auxiliary Functions? 3-17
Connecting the Programming Devices 3-18
Changing the CPU Mode of Operation 3-20

Clearing the CPU Memory 3–21
CPU Checklist 3–21

Chapter 4: Bases, Expansion Bases, and I/O Configuration

Understanding I/O Numbering and Module Placement Rules 4–2
DL305 I/O Configuration History 4–2
Octal Numbering System 4–2
Fixed I/O Numbering 4–2
I/O Numbering Guidelines 4–3
Number of I/O Points Required for Each Module 4–3
I/O Module Placement Rules 4–4
DL330/DL330P Rules for 16 Point Modules 4–5
DL340 Rules for 16 Point Modules 4–6
Base Specifications and Wiring 4–7
Three Sizes of Bases 4–7
Bases and Maximum I/O Supported 4–8
Base Mounting Dimensions 4–8
Connecting the Power Supply 4–9
Expansion Base Power Supply Wiring Example 4–9
Base Specifications 4–10
Auxiliary 24VDC Output at Base Terminal 4–10
Power Supply Schematics 4–11
Using the Run Relay on the Base Power Supply 4–12
Installing CPUs and I/O Modules 4–13
Using Bases for Local or Expansion I/O Systems 4–14
Base Uses Table 4–14
Local/Expansion Connectivity 4–14
Connecting Expansion Bases 4–15
Setting the Base Switches 4–16
5 Slot Bases 4–16
10 Slot Base 4–16
Example I/O Configurations 4–17
16 Point I/O Allocation Example 4–17
Examples Show Maximum I/O Points Available 4–17
I/O Configurations with a 5 Slot Local CPU Base 4–18
Switch settings 4–18
5 Slot Base with 8 Point I/O 4–18
5 Slot Base with 16 Point I/O 4–18
5 Slot Base and 5 Slot Expansion Base with 8 Point I/O 4–19
5 Slot Base and 5 Slot Expansion Base with 16 Point I/O 4–19
5 Slot Base and Two 5 Slot Expansion Bases with 8 Point I/O 4–20
5 Slot Base and Two 5 Slot Expansion Bases with 16 and 8 Point I/O 4–20
I/O Configurations with an 8 Slot Local CPU Base 4–21
8 Slot Base with 8 Point I/O 4–21
8 Slot Base with 16 Point I/O 4–21
8 Slot Base and 5 Slot Expansion Base with 8 Point I/O 4–21
8 Slot Base and 5 Slot Expansion Base with 16 Point I/O 4–21

I/O Configurations with a 10 Slot Local CPU Base 4–22
Switch settings 4–22
Last Slot Address Range 100 to 107 4–22
Last Slot Address Range 700 to 707 4–22
10 Slot Expansion Base with 16 Point I/O 4–23
Configuration 1 4–23
Configuration 2 4–23
10 Slot Base and 5 Slot Expansion Base with 16 Point I/O 4–24
Expansion Addresses Depend on Local CPU Base Configuration. 4–25
10 Slot Base and 10 Slot Expansion Base with 8 Point I/O 4–25
10 Slot Base and 10 Slot Expansion Base with 16 Point I/O 4–25
Calculating the Power Budget 4–26
Managing your Power Resource 4–26
Auxiliary Base Power Source 4–26
Base Power Specifications 4–26
Module Power Requirements 4–27
Power Budget Calculation Example 4–30
Power Budget Calculation Worksheet 4–31

Chapter 5: I/O Module Selection & Wiring Guidelines

I/O Selection Considerations 5–2
I/O Module Selection 5–2
Sinking and Sourcing Circuits 5–2
DL305 Input Module Configuration Chart 5–3
DL305 Output Module Configuration Chart 5–3
Configuration #1 DL305 DC Current Sourcing Input Module 5–4
Configuration #2 DL305 DC Current Sinking/Sourcing Input Module 5–4
Configuration #3 DL305 DC Current Sinking Input Module 5–5
Configuration #4 DL305 AC/DC Input Module 5–5
Configuration #5 DL305 AC Input Module 5–6
Configuration #6 DL305 DC Current Sinking Output Module 5–7
Configuration #7 DL305 DC Current Sourcing Output Module 5–7
Configuration #8 DL305 AC/DC Current Sink/Source (Relay) Output Module 5–8
Configuration #9 DL305 AC Output Module 5–8
Solid State Field Device Wiring to DC Input Modules 5–9
NPN Field Device Example 5–9
PNP Field Device Example 5–9
Derating Characteristics 5–10
I/O Wiring Guidelines 5–11
General Considerations 5–11
Wiring the Different Module Types 5–11
Fuse Protection 5–12
External Fuse Example 5–12

Chapter 6: Discrete Input Modules

Discrete Input Module Identification and Terminology 6-2
Discrete Input Module Status Indicators 6-2
Color Coding of I/O Modules 6-2
Input Module Selection 6-2
Inputs Per Module 6-3
Commons Per Module 6-3
Input Voltage Range 6-3
Peak Voltage 6-3
AC Frequency 6-3
ON Voltage Level 6-3
OFF Voltage Level 6-3
Input Current 6-3
Input Impedance 6-3
Minimum ON Current 6-3
Maximum OFF Current 6-3
Base Power Required 6-3
OFF to ON Response 6-3
ON to OFF Response 6-3
Terminal Type 6-3
Status Indicators 6-3
Weight 6-3
D3-08ND2, 24 VDC Input Module 6-4
D3-16ND2-1, 24 VDC Input Module 6-5
D3-16ND2-2, 24 VDC Input Module Module 6-6
D3-16ND2F, 24 VDC Fast Response Input Module 6-7
F3-16ND3F, TTL/24 VDC Fast Response Input Module 6-8
D3-08NA-1, 110 VAC Input Module 6-10
D3-08NA-2, 220 VAC Input Module 6-11
D3-16NA, 110 VAC Input Module 6-12
D3-08NE3, 24 VAC/DC Input Module 6-13
D3-16NE3, 24 VAC/DC Input Module 6-14
D3-08SIM, Input Simulator 6-15

Chapter 7: Discrete Output Modules

Discrete Output Module Identification and Terminology 7-2
Discrete Output Module Status Indicators 7-2
Color Coding of I/O Modules 7-2
Output Modules Selection 7-2
Outputs Per Module 7-3
Commons Per Module 7-3
Operating Voltage 7-3
Output Type 7-3
Peak Voltage 7-3

AC Frequency 7–3
ON Voltage Drop 7–3
Maximum Current (Resistive) 7–3
Maximum Leakage Current 7–3
Maximum Inrush Current 7–3
Minimum Load 7–3
Base Power Required 7–3
OFF to ON Response Time 7–3
ON to OFF Response Time 7–3
Terminal Type 7–3
Status Indicators 7–3
Fuses 7–3
Relay Life 7–3
Weight 7–3

Relay Arc Suppression – DC and AC Applications 7–4
FL305 High Current Relay Output Module Arc Suppression 7–4
Resistor and Capacitor Selection 7–4
Resistor Tolerance 7–4
Peak Voltage and Current 7–4
Adding Contact Protection 7–4
Resistor and Capacitor Nomogram 7–5

D3–08TD1, 24 VDC Output Module 7–6

D3–08TD2, 24 VDC Output Module 7–7

D3–16TD1–1, 24 VDC Output Module 7–8

D3–16TD1–2, 24 VDC Output Module 7–9

D3–16TD2, 24 VDC Output Module 7–10

D3–04TAS, 110–220 VAC Output Module 7–11

F3–08TAS, 250 VAC Isolated Output Module 7–12

F3–08TAS–1, 125 VAC Isolated Output Module 7–13

D3–08TA–1, 110–220 VAC Output Module 7–14

D3–08TA–2, 110–220 VAC Output Module 7–15

F3–16TA–2, 20–125 VAC Output Module 7–16

D3–16TA–2, 110–220 VAC Output Module 7–17

D3–08TR, Relay Output Module 7–18

F3–08TRS–1, Relay Output Module 7–19

F3–08TRS–2, Relay Output Module 7–20

D3–16TR, Relay Output Module 7–21

Chapter 8: System Operation

Introduction 8–2

CPU Operating System 8–3
DL305 CPU Operational Flow Chart 8–3

Initial Mode Setting and Memory Initialization **8–4**
Flow Chart for Initial Mode Setting (#1) 8–4
Memory Initialization 8–5
Program Mode Operation **8–6**
Run Mode Operation **8–7**
Update I/O Points 8–8
Service Peripherals 8–9
Service for Monitoring and Forcing Operations 8–10
Solve Application Program 8–12
I/O Response Time **8–14**
Is Timing Important for Your Application? 8–14
Normal Minimum I/O Response 8–14
Normal Maximum I/O Response 8–15
Improving Response Time 8–15
CPU Scan Time Considerations **8–16**
DL330 / DL330P Scan Calculation 8–16
DL340 Scan Calculation 8–17
Application Program Execution 8–18
Memory Map **8–19**
Octal Numbering System 8–19
Two Basic Memory Types: Discrete and Word 8–19
R Memory Locations for Discrete Memory Areas 8–20
I/O Points 8–20
Control Relays 8–21
Timers and Timer Status Bits (T Data type) 8–22
Counters and Counter Status Bits (CT Data type) 8–22
Counter Current Values (R Data Type) 8–22
Data Registers (R Data Type) 8–23
Stages (S Data type) 8–23
Shift Registers 8–24
Special Relays 8–24
Special Registers (R Data Type) 8–24
DL305 Aliases **8–25**
DL330 Memory Map 8–26
DL330P Memory Map 8–27
DL340 Memory Map 8–28
I/O Point Bit Map **8–29**
Control Relay Bit Map **8–30**
Special Relays **8–32**
Timer / Counter Registers and Contacts **8–33**
External Timer/Counter Setpoint Unit 8–33
Data Registers **8–34**
Stage Control / Status Bit Map **8–36**
Shift Register Bit Map **8–37**
Special Registers **8–38**

Chapter 9: Programming Basics

Introduction **9-2**

Using Boolean Instructions **9-3**
- END Statement 9-3
- Simple Rungs 9-3
- Normally Closed Contact 9-3
- Contacts in Series 9-4
- Midline Outputs 9-4
- Parallel Elements 9-4
- Joining Series Branches in Parallel 9-5
- Joining Parallel Branches in Series 9-5
- Comparative Boolean 9-5
- Combination Networks 9-6
- Boolean Stack 9-6

Using Timers **9-8**

Using Counters **9-9**

Using the Accumulator **9-10**
- Copying Data to and from the Accumulator 9-10
- Changing the Accumulator Data 9-11
- Accumulator Operations 9-12

Chapter 10: RLL*PLUS* Programming Basics

Introduction **10-2**

An Example Machine **10-3**
- Machine Operation 10-3
- Machine Flowchart 10-3

An RLL Solution **10-4**

An RLL*PLUS* Solution **10-5**

Stage Instruction Execution **10-6**
- Stage Instruction Numbering 10-6
- A Few Simple Rules for Execution 10-7

Activating Stages **10-8**
- Using Initial Stages 10-8
- Using Jump Instructions 10-9
- Using Set Instructions with Stages 10-10
- Power Flow Transitions 10-11

Using Outputs in Stages **10-12**
- Setting Outputs with the SET Instruction 10-12
- Using the OUT Instruction 10-13
- Latching Outputs with Stages 10-14

Using Timers and Counters in Stages **10-15**
- Time Based Transitions 10-15
- Using Counters 10-16

Using Data Instructions in Stages **10-17**

Using Comparative Contacts in Stages 10–18

Parallel Branching Concepts 10–19

Branching Methods 10–19
Joining Parallel Branches 10–20
Using Stage Bits as Contacts 10–21
Stage Contact Example 10–22

Unusual Operations in Stages 10–23

Using the Same Output Multiple Times 10–23
Using a Set Out Reset (SET OUT RST) Instruction 10–24
Output Placement 10–24

Two Ways to View RLL^{PLUS} Programs 10–25

DirectSOFT Stage View 10–25
DirectSOFT Ladder View 10–25

Designing a Program Using RLL^{PLUS} Instructions 10–27

Use ***Direct***SOFT to Save Time 10–27
Step 1: Design a Top-level Flowchart 10–28
Step 2: Add Flowchart Transitions 10–29
Step 3: Add Actions 10–30
Step 4: Add Conditions for Actions 10–31
Step 5: Add Alarm or Monitoring Operations 10–32
Step 6: Determine Stage Numbering 10–33
Step 7: Assign I/O Addresses 10–35
Step 8: Enter the Program 10–36

Chapter 11: Instruction Set

Introduction 11–2

Store (STR) 11–4
Store Not (STR NOT) 11–4
Store Timer (STR TMR) DL330/340 Only 11–5
Store Not Timer (STR NOT TMR) DL330/340 Only 11–5
Store Counter (STR CNT) DL330/340 Only 11–6
Store Not Counter (STR NOT CNT) DL330/340 Only 11–6
Or (OR) 11–7
Or Not (OR NOT) 11–7
Or Timer (OR TMR) DL330/340 Only 11–8
Or Not Timer (OR NOT TMR) DL330/340 Only 11–8
Or Counter (OR CNT) DL330/340 Only 11–9
Or Not Counter (OR NOT CNT) DL330/340 Only 11–9
And (AND) 11–10
And Not (AND NOT) 11–10
And Timer (AND TMR) DL330/340 Only 11–11
And Not Timer (AND NOT TMR) DL330/340 Only 11–11
And Counter (AND CNT) DL330/340 Only 11–12
And Not Counter (AND NOT CNT) DL330/340 Only 11–12
And Store (AND STR) 11–13
Or Store (OR STR) 11–13
Out (OUT) 11–15
Set (SET) DL330/340 Only 11–16
Reset (RST) DL330/340 Only 11–16

Set Out (SET OUT) 11–17
Set Out Reset (SET OUT RST) 11–18
Store If Equal (STR) DL330/DL340 Only 11–19
Store Not If Equal (STR NOT) DL330/DL340 Only 11–19
Or If Equal (OR) DL330/DL340 Only 11–20
Or Not If Equal (OR NOT) DL330/DL340 Only 11–20
And If Equal (AND) DL330/DL340 Only 11–21
And Not If Equal (AND NOT) DL330/DL340 Only 11–21
Timer (TMR) DL330/DL340 Only 11–22
Counter (CNT) DL330/DL340 Only 11–23
Shift Register (SR) DL330/340 Only 11–24
Data Store DSTR (F50) 11–25
Data Out DOUT (F60) 11–25
Data Store 1 DSTR (F51) 11–26
Data Out 1 DOUT (F61) 11–26
Data Store 2 DSTR (F52) 11–27
Data Out 2 DOUT (F62) 11–27
Data Store 3 DSTR (F53) 11–28
Data Out 3 DOUT (F63) 11–28
Data Store 5 DSTR (F55) 11–29
Data Out 5 DOUT (F65) 11–29
Data And DAND (F75) 11–30
Data Or DOR (F76) 11–31
Compare CMP (F70) 11–32
Add ADD (F71) 11–34
Add Example 11–35
Subtract SUB (F72) 11–36
Subtract Example 11–37
Multiply MUL (F73) 11–38
Multiply Example 11–39
Divide DIV (F74) 11–40
Divide Example 11–41
Shift Left SHFL (F81) 11–42
Shift Right SHFR (F80) 11–43

Number Conversion Instructions 11–44

Encode ENCOD (F83) 11–44
Decode DECOD (F82) 11–46
Binary BIN (F85) 11–47
Binary Coded Decimal BCD (F86) 11–48
Invert INV (F84) 11–49

Program Control Instructions 11–50

Master Control Set (MCS) and Master Control Reset (MCR) DL330/DL340 only 11–50
Understanding Master Control Relays 11–50
MCS/MCR Example 11–51

Network Instructions 11–52

Read from Network RX (F952) DL340 Only 11–52
Write to Network WX (F953) DL340 Only 11–54

Message Instructions 11–56

Fault FAULT (F20) 11–56

Chapter 12: RLL*PLUS* Instruction Set

Introduction **12-2**
Initial Stage (ISG) DL330P Only 12-3
Stage (SG) DL330P Only 12-3
Jump (JMP) DL330P Only 12-5
Not Jump (NOT JMP) DL330P Only 12-5
Store Stage (STR SG) DL330P Only 12-7
Store Not Stage (STR NOT SG) DL330P Only 12-7
Or Stage (OR SG) DL330P Only 12-8
Or Not Stage (OR NOT SG) DL330P Only 12-8
And Stage (AND Stage) DL330P Only 12-9
And Not Stage (AND NOT SG) DL330P Only 12-9
Set (SET) DL330P Only 12-10
Reset (RST) DL330P Only 12-10
Set Stage (SET SG) DL330P Only 12-11
Reset Stage (RST SG) DL330P Only 12-11
Comparative Boolean Instructions **12-12**
Store If Greater Than or Equal To Timer (STR TMR) DL330P Only 12-12
Store Not IF Greater Than Timer (STR NOT TMR) DL330P Only 12-12
Store If Greater Than or Equal To Counter (STR CNT) DL330P Only 12-13
Store Not If Greater Than Counter (STR NOT CNT) DL330P Only 12-13
Or If Greater Than or Equal To Timer (OR TMR) DL330P Only 12-14
Or Not If Greater Than Timer (OR NOT TMR) DL330P Only 12-14
Or If Greater Than or Equal To Counter (OR CNT) DL330P Only 12-15
Or Not If Greater Than Counter (OR NOT CNT) DL330P Only 12-15
And If Greater Than or Equal To Timer (AND TMR) DL330P Only 12-16
And Not If Greater Than Timer (AND NOT TMR) DL330P Only 12-16
And If Greater Than or Equal To Counter (AND CNT) DL330P Only 12-17
And Not If Greater Than Counter (AND NOT CNT) DL330P Only 12-17
Timer, Counter, and Shift Register Instructions **12-18**
Timer (TMR) DL330P Only 12-18
Counter (CNT) DL330P Only 12-19
Reset Counter (RST) DL330P Only 12-20
Shift Register (SR) DL330P Only 12-21

Chapter 13: Maintenance and Troubleshooting

Maintenance **13-2**
CPU Indicators **13-3**
Power Indicator **13-4**
RUN Indicator **13-6**
CPU Indicator **13-6**
BATT Indicator **13-6**
Expansion Base Power **13-7**
Testing Output Points **13-8**
I/O Module Troubleshooting **13-10**

Noise Troubleshooting **13–11**
Machine Startup and Program Troubleshooting **13–12**

Appendix A: Quick Start Example

Step 1: Unpack the DL305 Equipment **A–2**
Step 2: Configure the 5-slot Base as the Local CPU Base **A–3**
Step 3: Install the CPU and I/O Modules **A–3**
Step 4: Wire the I/O Modules to the Field Devices **A–4**
Step 5: Remove the Terminal Strip Access Cover **A–4**
Step 6: Connect the Power Wiring **A–5**
Step 7: Connect the Handheld Programmer **A–6**
Step 8: Connect the Power Source **A–7**
Step 9: Enter the Example Program **A–7**

Appendix B: DL305 Error Codes

Appendix C: Instruction Execution Times

Introduction **C–2**
Data Registers C–2
I/O Data Registers C–2
How to Read the Tables C–3
DL330 Instruction Execution Times **C–4**
Basic Input Instructions C–4
Output Type Instructions C–4
Timer, Counters, and Shift Registers C–5
Data Operations C–5
DL330P Instruction Execution Times **C–6**
Basic Input Instructions C–6
Output Type Instructions C–6
Timer, Counters, and Shift Registers C–6
Stage Instructions C–7
Data Operation Instructions C–7
DL340 Instruction Execution Times **C–8**
Basic Input Instructions C–8
Comparative Contacts C–8
Output Type Instructions C–8
Timer, Counters, and Shift Registers C–9
Data Operation Instructions C–9

Appendix D: DL305 Product Weight Tables

Product Weight Table **D–2**

Index